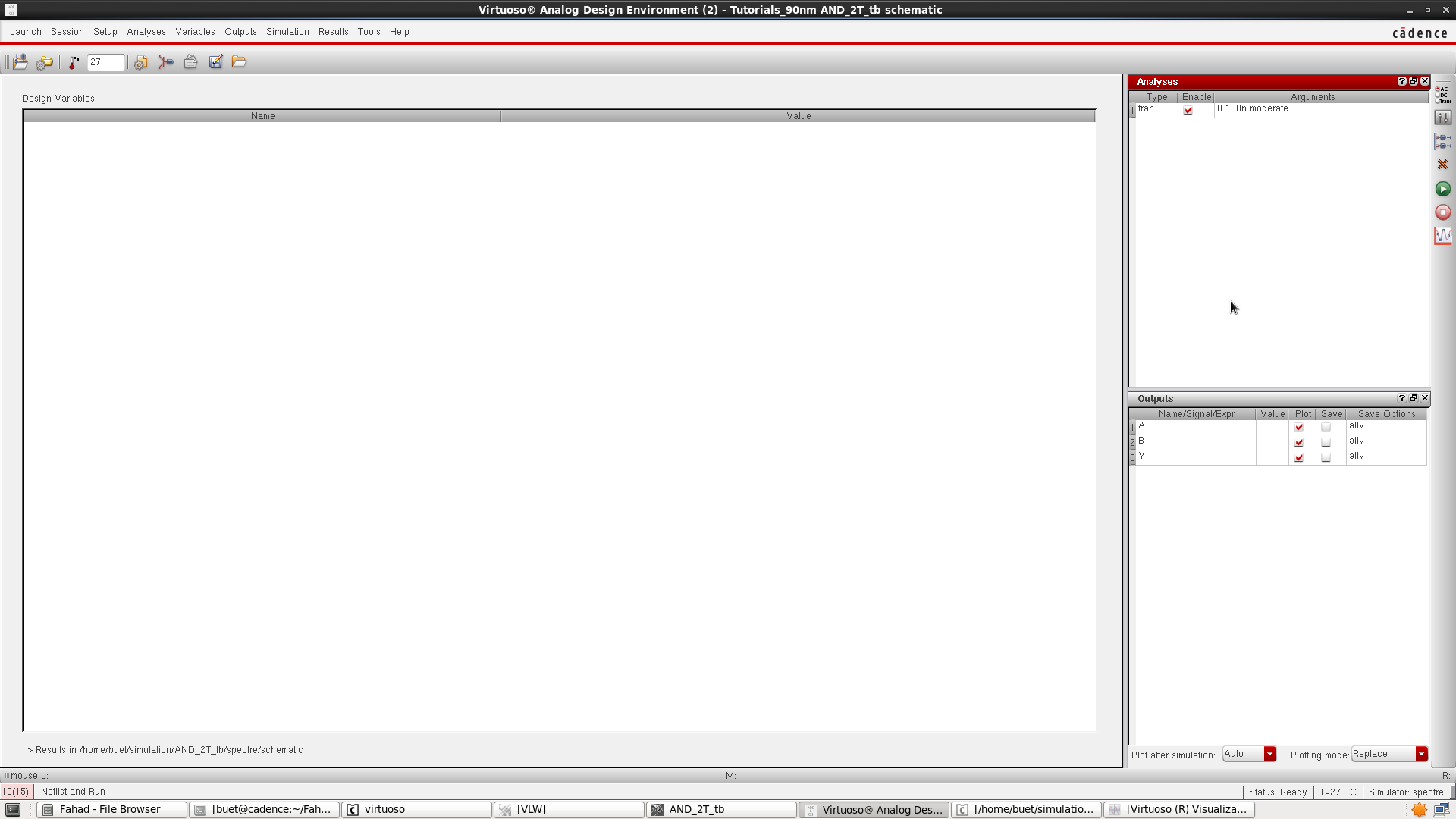
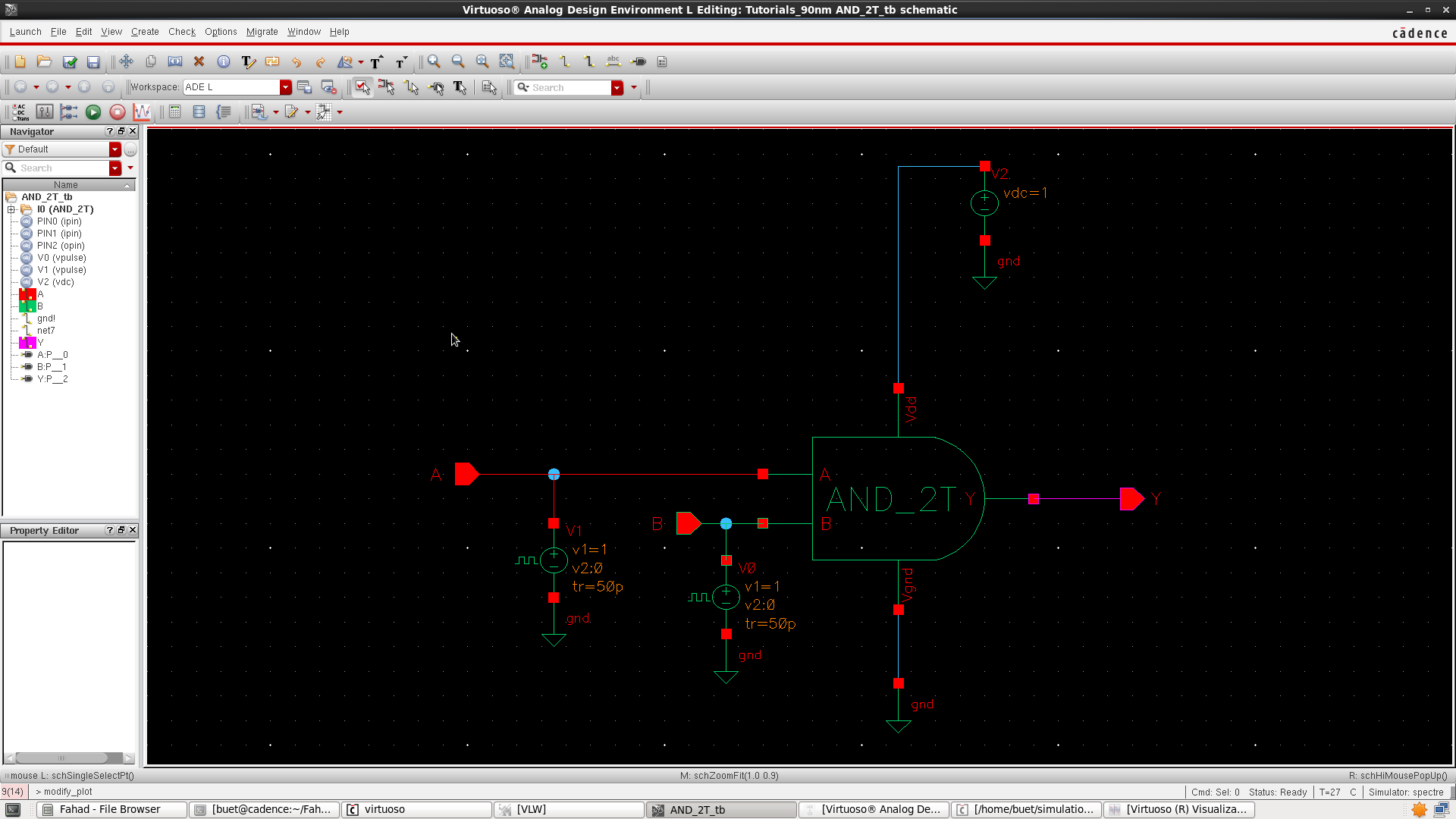
Job Name : implementation of AND gate and its transient analysis

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